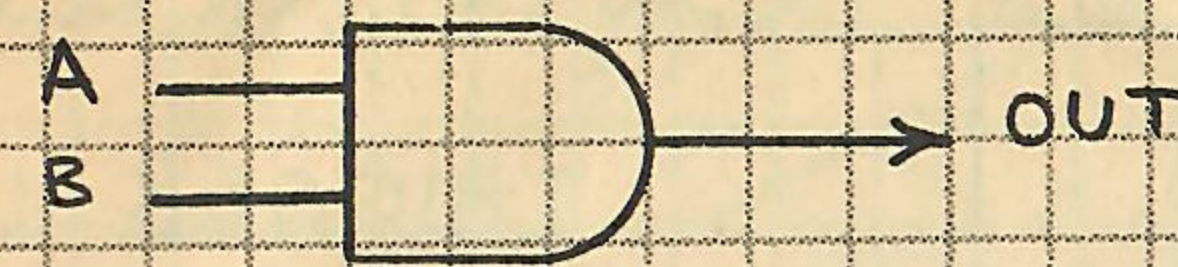


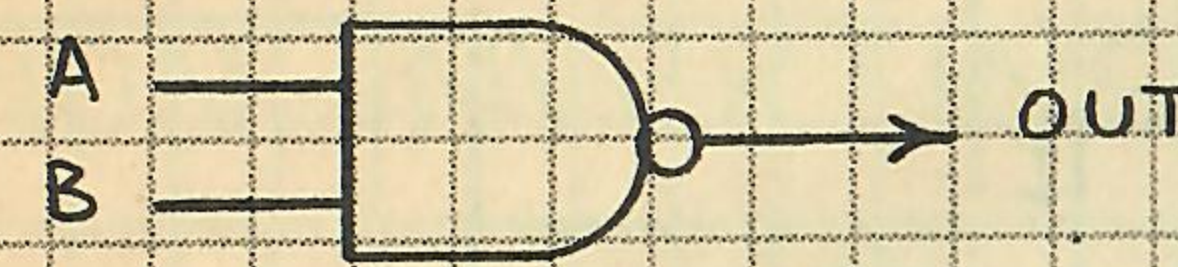
7. BASIC LOGIC CIRCUITS

AND GATE



A	B	OUT
L	L	L
L	H	L
H	L	L
H	H	H

NAND GATE



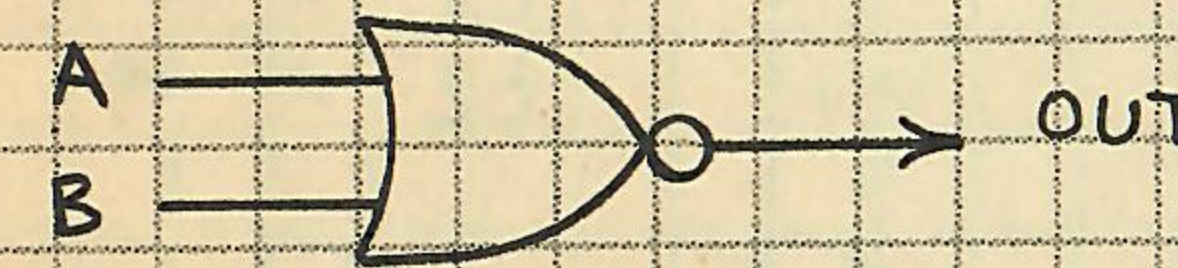
A	B	OUT
L	L	H
L	H	H
H	L	H
H	H	L

OR



A	B	OUT
L	L	L
L	H	H
H	L	H
H	H	H

NOR



A	B	OUT
L	L	H
L	H	L
H	L	L
H	H	L

EXCLUSIVE OR



A	B	OUT
L	L	L
L	H	H
H	L	H
H	H	L

EXCLUSIVE NOR



A	B	OUT
L	L	H
L	H	L
H	L	L
H	H	H

BUFFER (3-STATE BUFFER)



(C)	A	OUT
(L)	L	L
(L)	H	H
(H)	(X)	(HI-Z)

X = DON'T CARE

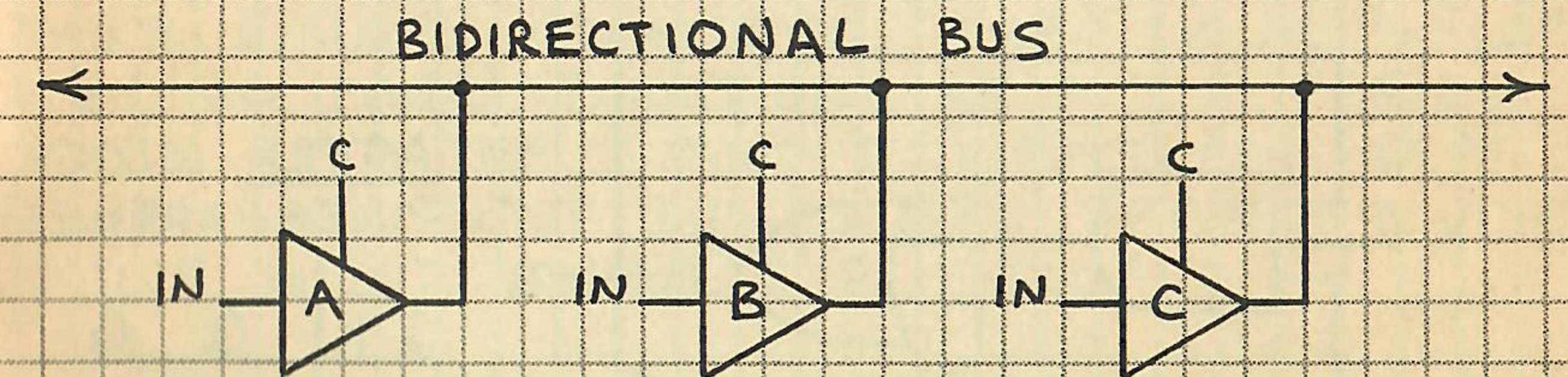
INVERTER (3-STATE INVERTER)



(C)	A	OUT
(L)	L	H
(L)	H	L
(H)	(X)	(HI-Z)

X = DON'T CARE

3-STATE BUS



COMPUTERS USUALLY HAVE A 3-STATE BUS.

CONTROL			GATE OUTPUT TO BUS
A	B	C	
L	H	H	A
H	L	H	B
H	H	L	C
H	H	H	NONE